

This article is available online at:

<http://www.cybernetman.com//kb/index.cfm/fuseaction/home.viewArticles/articleId/27>

### At What Voltage Would the CMOS Battery Lose System Settings?

#### **Cause - Possible power outage or just usage over time**

The CMOS battery will still perform its function successfully down to 2.6V. The cut-off electrical signal level for TTL/CMOS logic is 2.6V, defining the state as a 1, or HIGH state. Anything below that is a 0, or LOW state.